

09/802834

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,794,202 B2  
DATED : September 21, 2004  
INVENTOR(S) : Masud Beroz and David Light

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 1,

Line 25, after "integrated circuits" insert -- , --.

Line 52, delete "are" and insert therefor -- is --.

Column 4,

Line 53, after "combination" insert -- of --.

Column 5,

Line 33, delete ",".

Line 55, delete "in" and insert therefor -- is --.

Column 9,

Line 19, after "as" insert -- , --; after "chip" insert -- , --.

Column 10,

Line 21, after "shows" insert -- a --.

Line 32, after "preferably" insert -- are --.

Line 36, "dispose" should read -- disposed --.

Column 13,

Line 41, "Second" should read -- second --.

Column 15,

Line 33, delete "greeter" and insert therefor -- greater --.

Line 45, after "bending" insert -- the --.

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 16,  
Line 38, after "step" insert -- , --.

Signed and Sealed this

Eighth Day of February, 2005

A handwritten signature in black ink, reading "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D" at the end.

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*